

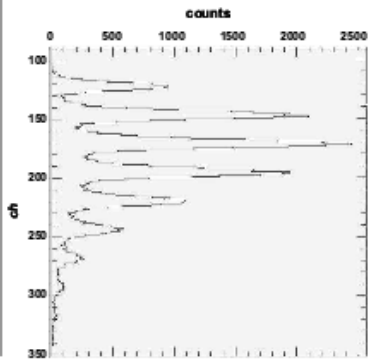
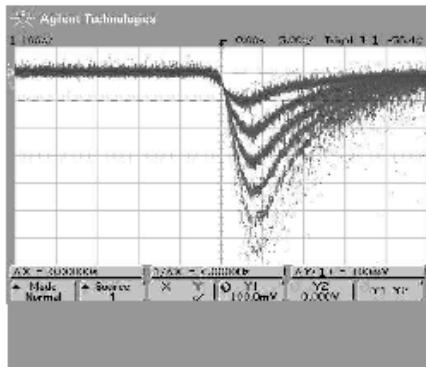
Створення лабораторної установки для роботи та тестування SiPM

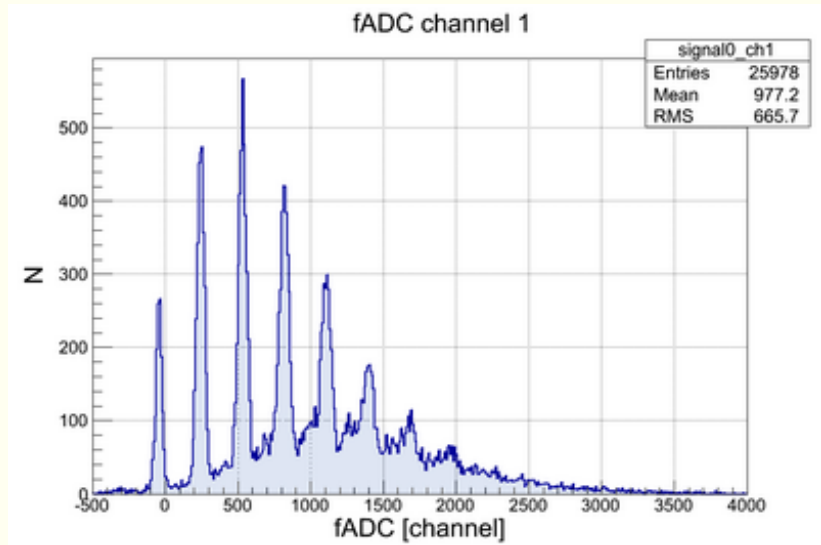
Ходневич Віталій

КНУ ім. Т. Шевченка

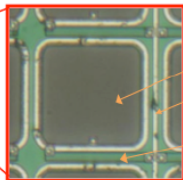
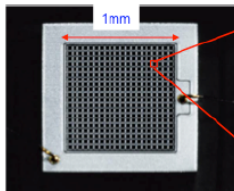
2015

SiPM





SiPM



GM-APD

Bias bus

Quench resistor

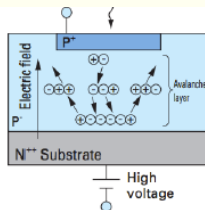


Figure 1: Photograph of a SiPM with 400 APDs

Figure 2: APD schematic

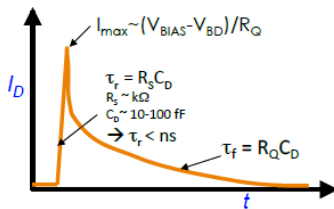
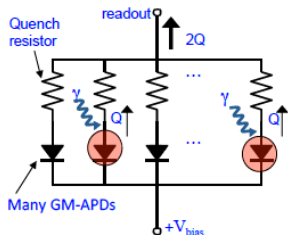
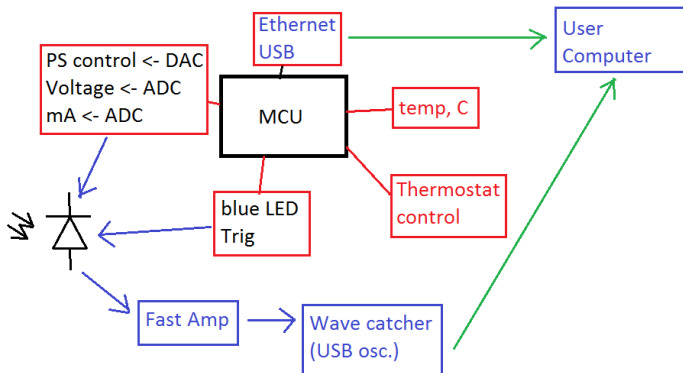
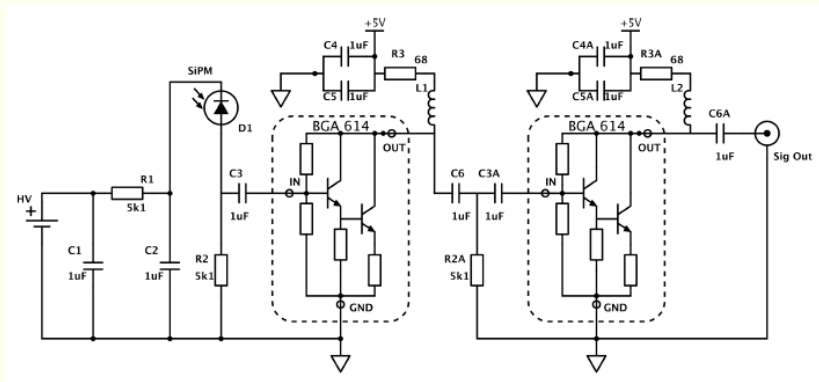


Figure 3: SiPM schematic (left) and current pulse initiated by an avalanche in an APD (right)

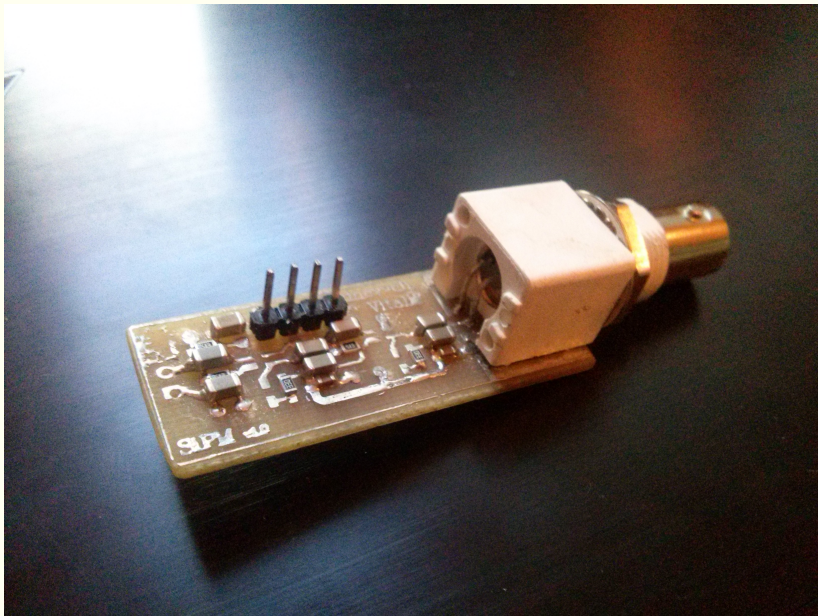
Блок-схема установки



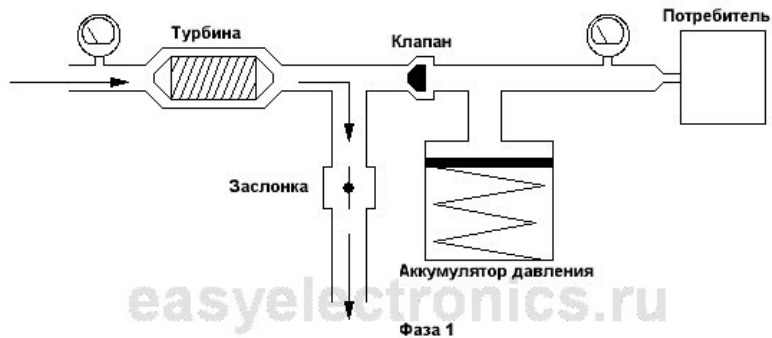
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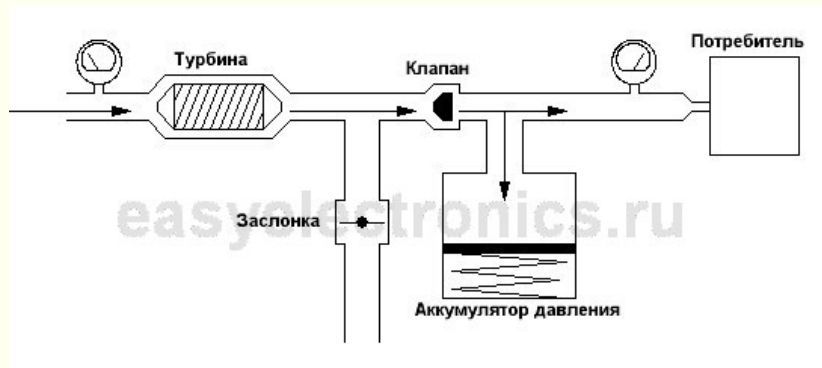
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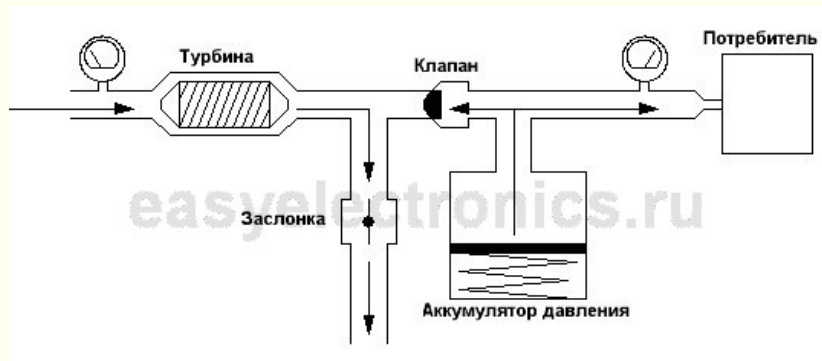
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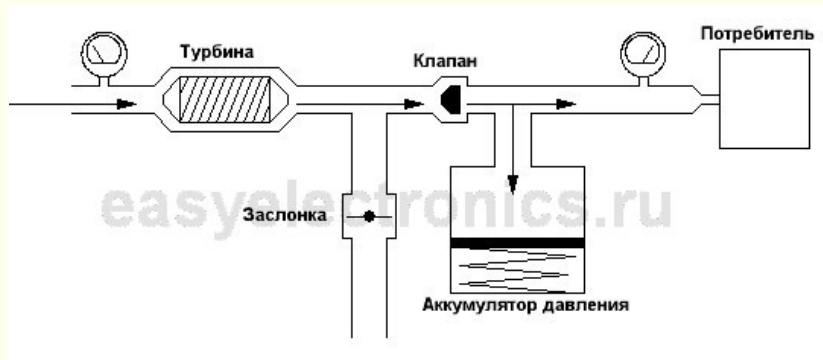
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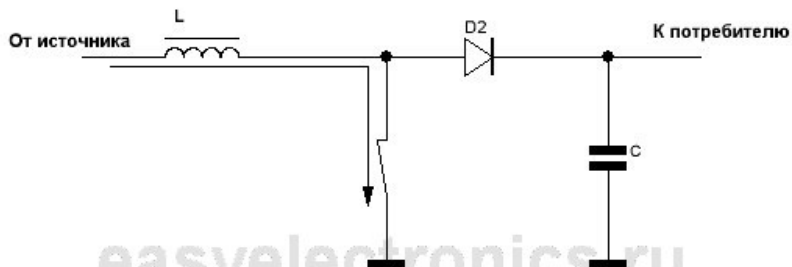
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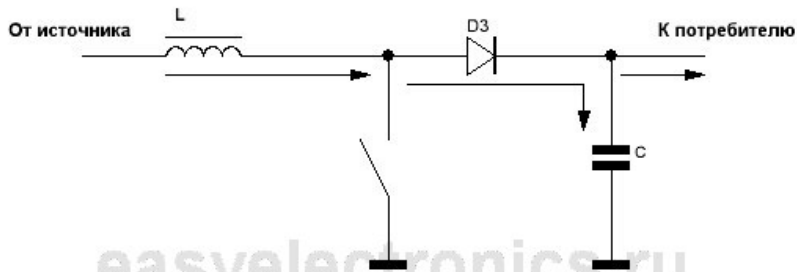
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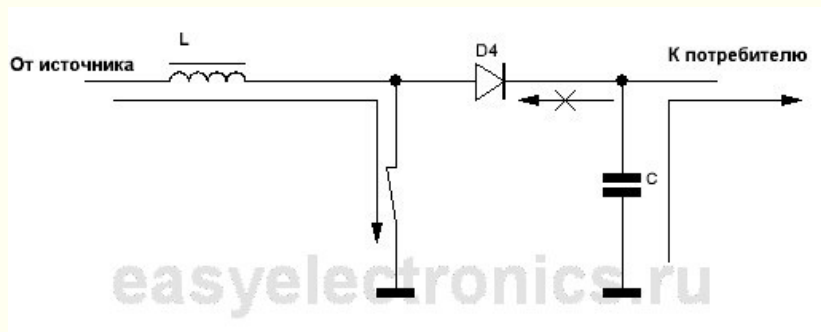
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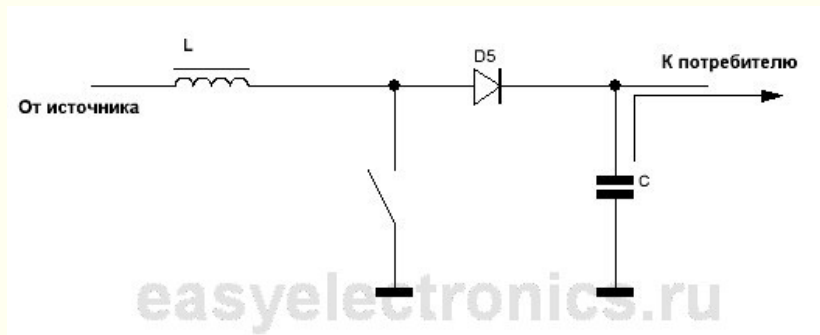
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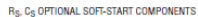
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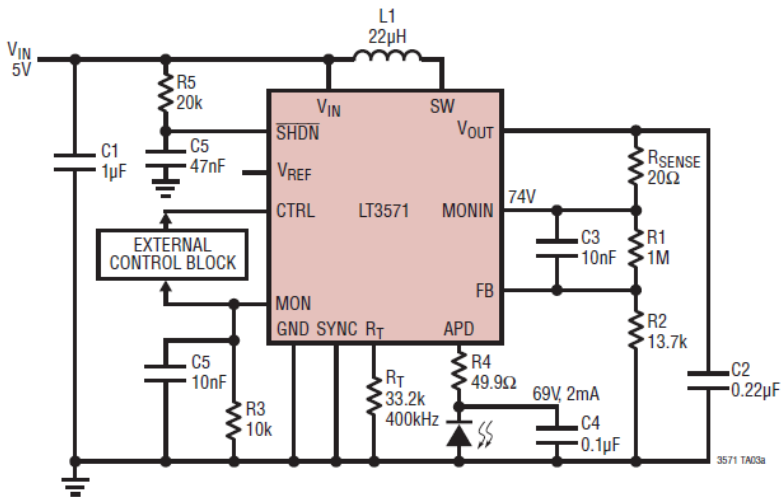


Бустер



Бустер

5V to 69V APD Bias Supply with Soft-Start



План надалі

- ▶ ADC
- ▶ DAC
- ▶ Digital switch
- ▶ Підсилювач
- ▶ Термостат